

VILASRAO DESHMUKH FOUNDATION GROUP OF INSTITUTIONS, LATUR

Plot No 165A, Additional MIDC, Near to Manjara Sugar, Barshi Road, Latur, Maharashtra 413531

(Approved by AICTE, New Delhi & recognized by Govt. of Maharashtra and affiliated to DBATU Lonere, Dist. Raigad)

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19. One-Day Workshop on PSPICE Software

Date: 09/05/2021

A one-day workshop on PSPICE software was conducted by Prof Puri S.A. to provide participants with a comprehensive understanding of its features and applications in the field of electrical and electronics engineering. The workshop aimed to equip participants with the necessary skills to design, simulate, and analyze electronic circuits using the PSPICE software. More than 53 students of electrical engineering participated in this workshop.

Introduction to PSPICE: The workshop began with an overview of PSPICE and its significance in circuit simulation. Participants gained insight into the capabilities of the software and its applications in various engineering domains.

Circuit Design and Simulation: The participants were introduced to the process of designing electronic circuits using PSPICE. They learned how to create schematic diagrams, select components, and simulate circuit behavior using the software.

Parameter Analysis: The workshop focused on parameter analysis techniques in PSPICE. Participants learned how to perform DC sweep, AC sweep, and transient analysis to evaluate circuit performance under different operating conditions.

Advanced Simulations: Advanced simulation techniques, such as Monte Carlo analysis and sensitivity analysis, were explored during the workshop. Participants gained knowledge on how to assess circuit reliability, analyze component tolerances, and optimize circuit designs.

Troubleshooting and Debugging: Participants learned strategies for troubleshooting and debugging circuits using PSPICE. They were introduced to the various tools and features available in the software to identify and resolve common issues.

Hands-on Exercises: The workshop included hands-on exercises where participants had the opportunity to apply their learning by working on real-world circuit design and simulation projects. They were guided by experienced facilitators throughout the exercises.

Conclusion:

The one-day workshop on PSPICE software proved to be a valuable learning experience for participants. They acquired essential skills in circuit design, simulation, and analysis using PSPICE, enhancing their proficiency in the field of electrical and electronics engineering. The hands-on exercises provided practical exposure, enabling participants to apply their knowledge in real-world scenarios. Overall, the workshop successfully achieved its objective of equipping participants with the necessary skills to utilize PSPICE effectively in their professional endeavors.



Vilasrao
Principal

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VDE SCHOOL OF ENGINEERING & TECHNOLOGY, LATUR
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Date-30/04/2021

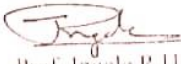
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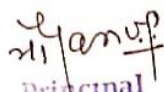
All the students of SE are here by informed that one day workshop is planned for PSPICE software on 09/05/2021 (Department of Electrical Engg.) for part-II of academic year 2020-21. Detail schedule is given below.

Time	Activity
9:00AM-10:00AM	Inauguration
10:30AM-12:30AM	Introduction
12:30PM-1:00PM	Lunch Break
1:00PM-2:00PM	Simulation Primer
2:00PM-3:00PM	Design Entry
3:00PM-4:00PM	Setting up and running analyses
4:00PM-5:00PM	Viewing results
5:00PM-6:00PM	Veledictory


Prof. Puri S.A.
Workshop




Prof. Ingale R.U.
H.O.D.


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Dr. Buke M.V.
Principal

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One Day Workshop - Attendance

Roll No.	Name of Students	
1	AGARKAR PAVANKUMAR RAHENDRA	P
2	BHOSLE VAIBHAV RAMRAO	P
3	CHAMBULE SHUBHANGI SUNIL	P
4	CHAVAN AASHLESHA BALAJI	P
5	CHAVAN SHRIKANT NITIN	P
6	CHINCHOLKAR AKASH SHANKARRAO	P
7	DESHMUKH ROHIT PRATAPRAO	P
8	DESHMUKH SUDARSHAN DHANRAJ	P
9	DONGARGAVE DHONDIRAM RAJESUMAR	P
10	GAVALI SANDHYARANI CHANDAR	P
11	GHODKE AKASH VASANT	P
12	GHOLE PRATHMESH PANDE	P
13	GONCHALE JYOTI UMAKANT	P
14	HERKAR ANKIT RAJKUMAR	P
15	JADHAV ABHISHEK DILIP	P
16	KADAM KIRAN UTTAMRAO	P
17	KAMBLE MIHAEL MILIND	P
18	KAMBLE PRADHUNYA DATTU	P
19	KAMBLE SURAJ SUNIL	P
20	KARAD YASHPAL ARVIND	P
21	KASAR HASAN MAGDUM	P
22	KHOSE AMRITA BALASAHEB	P
23	KHOSE BALAJI DATTATRAY	P
24	KSHIRSAGAR RENUKA GURUNATH	P
25	MANE RAMANANT	P
26	MANE SHIVPRANAV	P



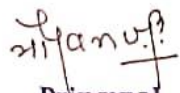
M. Ganap.
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27	MAZAKORI PARVEZ RAJJAK	P
28	MURTHI KAR MANI SURESH	P
29	MORE OMKAR PAPI	P
30	MUDPE POOJA SHIVKUMAR	P
31	NAKHATE NAYNATH SHIBKRISHNA	P
32	NIJESH SIVRUTIRAO KADAM	
33	PATHAN UBEDKHAN BASHIDKHAN	P
34	RATHOD NISHA DEEPAK	P
35	SALUNKE PATIL ABHINANDAN SUDHAKAR	P
36	SAWANT SUKANYA MADHAV	P
37	SAYYAD JAYAD LAYAK	P
38	SHAIKH BILAL NISAR	P
39	SHAIKH IRFAN YUSUF	P
40	SHAIKH MUSKAN MAHMOOD	P
41	SHAIKH NOORPASHA AHAMADPASHA	P
42	SHAIKH SHABAZ MAGDUM	P
43	SHIDORE MAYUR SIDDHESHVAR	P
44	SHINDE KARTIK SHASHIKANTI	P
45	SHINDE MANGESH MADHUKAR	P
46	SHINDE RAHESHVAR AMARDEEP	P
47	SHINGTE RUTHA DALLATRAY	P
48	TAMBHALE PATIL ADARV BALASAHEB	P
49	TAVDAR KESHAV DILIP	P
50	THORAT SAURABH SATISH	P
51	VAIRAGE RUPALI DADARAO	P
52	WADKAR ALASHI PRABHAKAR	P
53	WAGHMODE AMAR RAJEBHAU	P


Prof. Puri S.A.

Workshop Co-ordinator




Principal
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Prof. Ingale R.U.

H.O.D.